Name: \_\_\_\_\_\_ Date: \_\_\_\_\_

## Quiz 2:

## Problem 1:

Are the following true or false?

Question	True	False
The transfer function is computed from the LTI ODE with zero initial conditions		
The transfer block with the transfer function $e^{-s\Delta}$ corresponds to a delay		
of $\Delta$ The step response of a first-order lag can be written as $K\sigma(t)(1-e^{-t/T})$		
The step response of a second-order lag is always bounded		
There are different equivalent representations of transfer blocks		

## Problem 2:

We are given the following block diagram (left hand side) and its simplified structure (right hand side) Compute G(s).

